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March 4, 2005

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Brook Kebede - 2823

Kim-Marie Vo - 50,714

10/765,804— Paul A. Ingersoll et al.

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MESSAGE:

Freescale Semiconductor, Inc. Law Department 7700 W. Parmer Lane MD: TX32/PL02 Austin, TX 78729 Telephone: (512) 996-6839 Facsimile: (512) 996-6854 Number of Pages (including this page) United States Patent and Trademark Office NOTICE: This facsimile transmission may contain information that is confidential, privileged, or exempt from disclosure under applicable law. It is intended only for the person to whom it is addressed. Unauthorized use, disclosure, copying or distribution may expose you to legal liability. If you have received this transmission in error, please immediately notify us by telephone (collect) to arrange for return of the documents received and Enclosed herewith, please find a FORMAL AMENDMENT for filing in the below-identified application. Paid by Deposit Account: 503079, Freescale Semiconductor, Inc. I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE:

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EXAMINER: Brook Kebede **GROUP ART UNIT: 2823** SERIAL NO.: 10/765.804 FILED: JANUARY 27, 2004

INVENTOR: PAUL A. INGERSOLL

## UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)

Paul A. Ingersoll et al.

**GROUP ART UNIT:** 

2823

APPLN. NO.:

10/765,804

EXAMINER: Brook Kebede

FILED:

January 27, 2004

TITLE:

METHOD FOR FORMING A MEMORY STRUCTURE USING A

MODIFIED SURFACE TOPOGRAPHY AND STRUCTURE THEREOF

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## RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated February 15, 2005, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.